

## **AMENDMENTS TO THE CLAIMS:**

Please amend claims 4-8 as follows:

1. (Original) An integrated silicon electrode for a battery, comprising a regular or irregular array of sub-micron silicon structures fabricated on a silicon substrate.
2. (Original) A silicon anode comprising an electrode according to claim 1, in which the sub-micron silicon structures comprise pillars of silicon fabricated on an n-type silicon substrate.
3. (Original) A silicon anode according to claim 2 made on a wafer-bonded silicon-on-insulator substrate.
4. (Currently Amended) A silicon anode according to claim 2 ~~or claim 3~~ in which the silicon pillars do not exceed a fractional coverage of 0.5 of the substrate.
5. (Currently Amended) A silicon electrode according to ~~any one of the preceding claims~~ claim 1 formed by the steps of:
  - (a) depositing a very thin film of a highly soluble solid onto a flat hydrophilic silicon substrate;
  - (b) exposing the film to solvent vapour under controlled conditions so that the film reorganizes into an array of discrete hemispherical islands on the surface; and
  - (c) reactively ion etching the silicon substrate with the islands of highly soluble solid acting as a resist so that the exposed silicon is etched away leaving pillars corresponding to the islands.
6. (Currently Amended) A silicon anode according to ~~any one of the preceding claims~~ claim 2 wherein the pillars are 0.1-1.0 microns in diameter (d) and 1-10 microns in height (H).
7. (Currently Amended) A silicon anode according to ~~any one of the preceding~~

~~claims claim 6~~ wherein the pillars are ~~~0.3~~ about 0.3 microns in diameter (d) and about 6 microns in height (H).

8. (Currently Amended) A lithium battery including an anode in accordance with ~~any one of the preceding claims~~ claim 1.